



DOCKET NO.: MSFT-2865/304048.02

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Steve Pronovost, Anuj B. Gosalia,  
Bryan L. Langley and Hideyuki Nagase

Confirmation No.: Not Yet Assigned

Application No.: 10/777,797

Group Art Unit: Not Yet Assigned

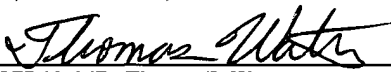
Filing Date: February 12, 2004

Examiner: Not Yet Assigned

For: **SYSTEMS AND METHODS FOR SCHEDULING COPROCESSOR  
RESOURCES IN A COMPUTING SYSTEM**

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TYPED NAME: Thomas E. Watson  
REGISTRATION NO.: 43,243

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**INFORMATION DISCLOSURE STATEMENT**

Pursuant to 37 CFR § 1.56 and in accordance with 37 CFR §§ 1.97-1.98, information relating to the above-identified application is hereby disclosed. Inclusion of information in this statement is not to be construed as an admission that this information is material as that term is defined in 37 CFR § 1.56(b).

- ☒ In accordance with § 1.97(b), since this Information Disclosure Statement is being filed either within three months of the filing date of the above-identified application, within three months of the date of entry into the national stage of the above identified application as set forth in § 1.491, before the mailing date of a first Office Action on the merits of the above-identified application, or before the mailing date of a first Office Action after the filing of request for continued examination under § 1.114, no additional fee is required.

- ☐ In accordance with § 1.129(a), this Information Disclosure Statement is being filed in connection with ☐ the first or ☐ second After Final Submission, therefore:
- ☐ Certification in Accordance with § 1.97(e) is attached; or
- ☐ The fee of \$180.00 as set forth in § 1.17(p) is attached.
- ☐ In accordance with § 1.97(c), this Information Disclosure Statement is being filed after the period set forth in § 1.97(b) above but before the mailing date of either a Final Action under § 1.113 or a Notice of Allowance under § 1.311, or before an action that otherwise closes prosecution in the application, therefore:
- ☐ Certification in Accordance with § 1.97(e) is attached; or
- ☐ The fee of \$180.00 as set forth in § 1.17(p) is attached.
- ☐ In accordance with § 1.97(d), this Information Disclosure Statement is being filed after the mailing date of either a Final Action under § 1.113 or a Notice of Allowance under § 1.311 but before, or simultaneously with, the payment of the Issue Fee, therefore included are: Certification in Accordance with § 1.97(e); and the submission fee of \$180.00 as set forth in § 1.17(p).
- ☒ Copies of each of the references listed on the attached Form PTO-1449 are enclosed herewith.
- ☐ Copies of references listed on the attached Form PTO-1449 are enclosed herewith EXCEPT THAT:
- ☐ Copies of references listed on the attached Form PTO 1449 are not required to be submitted pursuant to the June 30, 2003 recent revisions to 37 CFR § 1.98(a)(2)(i).

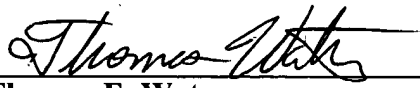
- ☐ In view of the voluminous nature of references [list as appropriate], and the likelihood that these references are available to the Examiner, copies are not enclosed herewith.
- ☐ In accordance with § 1.98(d), copies of the following references listed on the attached Form PTO-1449 are not enclosed herewith because they were previously cited by or submitted to the U.S. Patent and Trademark Office in patent application(s) for which a claim for priority under 35 U.S.C. § 120 have been made in the instant application:
- ☐ Copies of references [list as appropriate] listed on the attached Form PTO-1449 were previously cited by or submitted to the Patent and Trademark Office in prior Application No. , filed

Please charge any deficiency or credit any overpayment to Deposit Account No. 23-3050.

This form is submitted in duplicate.

There are no listed references which are not in the English language.

Date: 3/25/04

  
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<b>Form PTO-1449 Modified</b>  List of Patent and Publications Cited by Applicant (Use several sheets if necessary)  U.S. Department of Commerce Patent and Trademark Office	Docket No. <b>MSFT-2865/304048.02</b>		Application No. <b>10/777,797</b>	
	Applicant <b>Steve Pronovost, Anuj B. Gosalia, Bryan L. Langley and Hideyuki Nagase</b>			
	Filing Date <b>February 12, 2004</b>		Group <b>Not Yet Assigned</b>	
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>				
	<b>1</b>	Burgess, P. et al., "BED:A Multithread Kernel for Embedded Systems", <i>Real Time Programming, IFAC Workshop, 1994</i> , 133-138		
	<b>2</b>	Macedonia, M., "The GPU Enters Computing's Mainstream", <i>Computer</i> , <b>October 2003</b> , 36(10), 106-108		
	<b>3</b>	Petrot, F. et al., "Lightweight Implementation of the POSIX Threads API for an On-Chip MIPS Multiprocessor with VCI Interconnect", <i>Proceedings Design, Automation and Test in Europe Conference and Exhibition, March 3-7, 2003</i> , Munich, Germany, 51-56		
<b>EXAMINER</b>		<b>DATE CONSIDERED</b>		